

AMENDMENT TO THE SPECIFICATION

Please amend the paragraph beginning on page 1, line 16 as follows:

*A2* --Figure 11 is a diagram showing the rough structure of a general microprocessor. As shown in the diagram, the program counter 10 supplies an instruction storage unit 100 composed of a read-only-memory (ROM) 101, or random-access-memory (RAM), ROM 101 (or RAM) with the address of an instruction to be executed next. The instruction storage unit 100 outputs instruction data in accordance with the received address to a decoder 40. Through these operations, the processes are executed in sequence.--